AD5710R Data Sheet Revision

AD5710R Specification Change

From Rev. 0

Parameter	Min	Max	Units	Conditions
t_1 (Write)	20		ns	SCLK cycle period write (Note # 1)
t_1 (Read)	40		ns	SCLK cycle period read (Note # 2)

#1. Equivalent to 50MHz for write operation only

#2. Equivalent to 25MHz for read operation only

To Rev. A

Parameter	Min	Max	Units	Conditions
t_1 (Write)	20		ns	SCLK cycle period write (Note # 1)
t_1 (Read)	40		ns	SCLK cycle period read (Note # 2)
t_1		80	ns	(Note # 3)

#1. Equivalent to 50MHz for write operation only

#2. Equivalent to 25MHz for read operation only

#3. Only applicable for Input and DAC register updates while in streaming mode with LOOP_COUNT > 0. Refer to Streaming mode section for more details.

Figure 2. Serial Read and Write Operation